

**INFORMATION DISCLOSURE  
STATEMENT**

 Atty Docket:  
 Serial No.:  
 Applicant:  
 Filing Date:  
 Group:

 00RO27054366  
 09/989,317  
 Mariaud et al.  
 November 20, 2001

**U.S. PATENT DOCUMENTS**

Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
<i>CEL</i>	AA	5,974,486	10/26/1999	Siddappa	710	53	
<i>CEL</i>	AB	6,205,501	3/20/2001	Brief et al.	710	100	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

**FOREIGN PATENT DOCUMENTS**

		Document Number	Date	Country	Class	Sub Class	Translation
<i>CEL</i>	AM	19900369	7/8/1999	DE	G06F13	12	English Abstract
	AN						
	AO						
	AP						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)**

<i>CEL</i>	AQ	Intel Corporation, 8x931AA, 8x931HA Universal Serial Bus Peripheral Controller User's Manual, September 1997, Intel Corporation, XP002166357.					
<i>CEL</i>	AR	STMicroelectronics, Using the ST7 Universal Serial Bus Microcontroller, 1998, STMicroelectronics XP002166358.					
<i>CEL</i>	AS	National Semiconductor, USBN9602 (Universal Serial Bus) Full Speed Function Controller With DMA Support, November 1998, National Semiconductor, XP002166359.					

EXAMINER:

*Christopher E. Lee*

DATE CONSIDERED:

*5/20/04*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.